

	L #	Hit	Search Text	DB
1	L1	1297	29/840.ccls.	USP AT; US-P GPU B
2	L2	999	29/830.ccls.	USP AT; US-P GPU B
3	L3	2163	1 2	USP AT; US-P GPU B
4	L4	139	3 and (interposer\$1 translator adapter adaptor)	USP AT; US-P GPU B
5	L5	127	4 and (solder\$4 pb sn lead tin)	USP AT; US-P GPU B
6	L6	12	4 not 5	USP AT; US-P GPU B
7	L7	2024	3 not 4	USP AT; US-P GPU B

	L #	Hit	Search Text	DBs
8	L8	363	7 and ((pb with sn) (lead with tin))	USP AT; US-P GPU B
9	L9	1800	3 not 8	USP AT; US-P GPU B
10	L10	1188	9 and solder\$4	USP AT; US-P GPU B
11	L11	147	10 and column\$2	USP AT; US-P GPU B
12	L12	101	11 not (4 8)	USP AT; US-P GPU B
13	L13	1041	10 not 11	USP AT; US-P GPU B
14	L14	416	13 and (through adj hole\$1)	USP AT; US-P GPU B

	L #	Hit	S arch Text	DB
15	L15	625	13 not 14	USP AT; US-P GPU B
16	L16	24	15 and (conductive adj via\$1)	USP AT; US-P GPU B
17	L17	601	15 not 16	USP AT; US-P GPU B
18	L18	155	17 and (carrier (interconnect\$3 adj board\$1))	USP AT; US-P GPU B
19	L19	446	17 not 18	USP AT; US-P GPU B

	L #	Hit	earch Text	DB
1	L1	1444	(361/767,768,769,771,803).ccls.	USP AT; US-P GPU B
2	L2	1785	(174/262,263,264,266).ccls.	USP AT; US-P GPU B
3	L3	5321	(257/698,723,737,738,72,778,779,785).ccls.	USP AT; US-P GPU B
4	L4	8020	1 2 3	USP AT; US-P GPU B
5	L5	891	4 and @pd>=20030322	USP AT; US-P GPU B
6	L8	143	5 and (lead with tin)	USP AT; US-P GPU B
7	L6	92	5 and (pb same sn)	USP AT; US-P GPU B

	L #	Hit	earch Text	DB
8	L7	40	5 and (lead with pb)	USP AT; US-P GPU B
9	L9	104	8 not (6 7)	USP AT; US-P GPU B
10	L11	1	10 and (lead with tin)	USP AT; US-P GPU B
11	L10	1	6198634.pn.	USP AT; US-P GPU B
12	L12	689	5 not (6 7 8)	USP AT; US-P GPU B
13	L13	94	12 and (interposer\$1 translator adapt\$1r)	USP AT; US-P GPU B
14	L14	595	12 not 13	USP AT; US-P GPU B

	L #	Hit	Search Text	DB
15	L15	433	14 and solder\$4	USP AT; US-P GPU B
16	L16	119	15 and carrier\$1	USP AT; US-P GPU B
17	L17	119	16 not (6 7 8 13)	USP AT; US-P GPU B
18	L18	314	15 not 16	USP AT; US-P GPU B
19	L19	38	18 and (castellation\$1 grooves notch\$2)	USP AT; US-P GPU B
20	L20	239	18 and ((through adj hole\$1) via\$1)	USP AT; US-P GPU B
21	L21	205	20 not 19	USP AT; US-P GPU B